



101809180

COFC

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent No.:	7234030
Issued:	June 19, 2007
First Named Inventor:	Navdhish Gupta
Title	TABLE-BASED SCHEDULER FOR FIFOS AND THE LIKE

**REQUEST FOR ISSUANCE OF CERTIFICATE OF CORRECTION**  
**PURSUANT TO 37 CFR 1.323**

Certificate

APR 29 2008

of Correction

Certificate of Corrections Branch  
Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Review of the above-identified patent has revealed errors in the patent, one of which is attributable to applicants. Applicants' error is of a clerical or typographical nature (the word "selected" was inadvertently deleted from claim 13 in preparing the Response To Final Rejection) and its proposed correction does not constitute new matter or require reexamination. Applicants therefore request that a Certificate Of Correction be issued to correct these errors.

The locations of the errors in the patent are set forth below:

Errors in Patent	Proposed Correction
Col. 9, line 36: "feedback logic;"	-- feedback logic: -- (PTO error. See Response to Final Rejection, page 4, claim 11, line 13)
Col 10, line 9: "currently FIFO"	-- currently selected FIFO -- (Applicants' error. See Response To Final Rejection, page 4, claim 13, lines 4 and 8-9 and Amendment filed 9/25/06, page 4, claim 13, line 3)

RECEIVED-USPTO  
Patent Publication


APR 29 2008

Documentation supporting this request and a form PTO/SB/44 showing the corrections are enclosed.

The Commissioner is hereby authorized to charge any fees associated with this communication to Deposit Account No. 501958.

Respectfully submitted,

Date: 4/22/08

By:   
Mark L Becker  
Associate General Counsel, IP  
Reg. No. 31,325  
Customer No. 29416

Lattice Semiconductor Corporation  
5555 NE Moore Court  
Hillsboro, OR 97124  
Phone: 503-268-8629  
Fax: 503-268-8077

RECEIVED-USPTO  
Patent Publication

APR 29 2008

## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

Page 1 of 1

PATENT NO. : 7,234,030 B1

APPLICATION NO.: 10/809,180

ISSUE DATE : June 19, 2007

INVENTOR(S) : Navdhish Gupta, Gary Allen

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 9, line 36: replace "feedback logic;" with -- feedback logic: --.

Col 10, line 9: replace "currently FIFO" with -- currently selected FIFO --.

### MAILING ADDRESS OF SENDER (Please do not use customer number below):

Lattice Semiconductor Corporation  
5555 NE Moore Ct.  
Hillsboro, Oregon 97124

This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: **Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

RECEIVED USPTO  
Patent Publication

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

APR 29 2008

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

First Named Inventor: Navdhish Gupta  
Application No.: 10/809,180  
Filed: 03/25/2004  
For TABLE-BASED SCHEDULER FOR FIFOS AND THE  
LIKE  
Examiner: Woo H Choi  
Art Unit: 2189  
Confirmation No. 6823  
Attorney Docket No. 1054.028

**RESPONSE TO FINAL REJECTION**

Applicants respond to the final Office action mailed 12/19/2006 as follows.

RECEIVED-USPTO  
Patent Publication

APR 29 2008

8 11. (currently amended) A method for scheduling service for a plurality of ~~packet storage devices~~ FIFOs, the method comprising:

- accessing a look-up table (LUT) stored in a ROM with an input address to retrieve a LUT output, wherein:
  - the input address corresponds to current status of one or more of the ~~packet storage devices~~ FIFOs; and
  - the LUT output identifies a next ~~packet storage device~~ FIFO to select for service and whether the ~~packet storage device~~ FIFO has data available for service;
  - storing and forwarding the LUT output based on a received latch enable (LE) control signal;
  - with a finite state machine (FSM) implemented using combinatorial feedback logic:
    - generating the LE control signal based on the forwarded LUT output; and
    - generating a read enable (RE) control signal that identifies which one or more of ~~packet storage devices~~ FIFOs are to be serviced, based on the forwarded LUT output.

12. (canceled)

9 13. (currently amended) The invention of claim <sup>8</sup> 11, wherein the LE and RE control signals are generated using an FSM having an IDLE state and an EXTRACT state, wherein:

- when the FSM is in the IDLE state and a currently <sup>selected</sup> ~~packet storage device~~ FIFO has no data available for service, the LE control signal is set to forward the LUT output; and
- when the FSM is in the EXTRACT state and service of the currently selected ~~packet storage device~~ FIFO is completed, the LE control signal is set to forward the LUT output.

RECEIVED-USPTO  
Patent Publication

APR 29 2008

Remarks


Reconsideration of the application is requested. Applicants have amended claim 1 to incorporate the allowable subject matter of claim 2 and have amended claim 11 to include the allowable subject matter of claim 12. Claim 1 and its remaining dependent claims and claim 11 and its remaining dependent claims should now be allowable.

Applicants submit that the application is now in condition for allowance, and such action is requested.

Please call the undersigned if he can be of any assistance in completing this case.

Respectfully submitted,

Date: 3/13/07

By: 

Mark L. Becker  
Reg. No. 31325  
Associate General Counsel, IP

Customer No. 29416  
Lattice Semiconductor Corporation  
5555 NE Moore Ct.  
Hillsboro, OR 97124  
Phone: 503-268-8629  
Fax: 503-268-8459  
mark.becker@latticesemi.com

RECEIVED-USPTO  
Patent Publication

APR 29 2008

CUSTOMER NO. 22186

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re: Attorney Docket No. 1054.028

In re application of: Navdhish Gupta and Gary D. Allen

Serial No.: 10/809,180

Group Art Unit: 2189

Filed: 03/25/04

Examiner: Woo H. Choi

Matter No.: L03-048

Phone No.: 571-272-4179

For: Table-Based Scheduler for FIFOs and the Like

AMENDMENT UNDER 37 CFR 1.111

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

This Amendment is filed in response to the non-final office action of 06/09/06.

\*

\*

\*

\*

\*

Certification Under 37 CFR 1.8

Date of Deposit September 25, 2006

I hereby certify that this correspondence is being deposited in the United States Postal Service with sufficient postage as first class mail under 37 CFR 1.8 on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

RECEIVED-USPTO  
Patent Publication

APR 29 2008

MARY E. CANIZ.  
(Name of person mailing)

Mary E. Caniz.  
(Signature of person mailing)

2 the packet storage devices are FIFOs;  
3 the LUT is stored in a ROM; and  
4 the LE and RE control signals are generated using a finite state machine (FSM) implemented  
5 using combinatorial feedback logic.

1 13. (original) The invention of claim 11, wherein the LE and RE control signals are  
2 generated using an FSM having an IDLE state and an EXTRACT state, wherein:  
3 when the FSM is in the IDLE state and a currently selected packet storage device has no data  
4 available for service, the LE control signal is set to forward the LUT output; and  
5 when the FSM is in the EXTRACT state and service of the currently selected packet storage  
6 device is completed, the LE control signal is set to forward the LUT output.

1 14. (original) The invention of claim 13, wherein:  
2 when the FSM is in the IDLE state and at least one packet storage device has data available for  
3 service, the FSM transitions to the EXTRACT state; and  
4 when the FSM is in the EXTRACT state and no packet storage device has data available for  
5 service, the FSM transitions to the IDLE state.

1 15. (original) The invention of claim 11, wherein the current status of the one or more  
2 packet storage devices comprises an indication of whether each packet storage device has data available  
3 for service and an indication of which packet storage device is currently selected for service.

1 16. (currently amended) The invention of claim 15, wherein a packet storage device has data  
2 available for service when the packet storage device currently stores more than a specified non-zero  
3 threshold number of data packets.

1 17. (original) The invention of claim 11, wherein the LE and RE control signals are  
2 generated based on service status information from the packet storage devices.

1 18. (original) The invention of claim 17, wherein the service status information comprises  
2 an indication of completion of service of the currently selected package storage device.

RECEIVED-USPTO  
Patent Publication

APR 29 2008



9

when the FSM is in the EXTRACT state and service of the currently selected FIFO is completed, the FSM sets the LE control signal to enable the latch to forward the LUT output.

3. The invention of claim 2, wherein:

when the FSM is in the IDLE state and at least one FIFO has data available for service, the FSM transitions to the EXTRACT state; and

when the FSM is in the EXTRACT state and no FIFO has data available for service, the FSM transitions to the IDLE state.

4. The invention of claim 1, wherein the current status of the one or more FIFOs comprises an indication of whether each FIFO has data available for service and an indication of which FIFO is currently selected for service.

5. The invention of claim 4, wherein a FIFO has data available for service when the FIFO currently stores more than a specified threshold number of data packets.

6. The invention of claim 1, wherein the FSM is further adapted to receive service status information from the FIFOs.

7. The invention of claim 6, wherein the service status information comprises an indication of completion of service of the currently selected FIFO.

8. A method for scheduling service for a plurality of FIFOs, the method comprising:

accessing a look-up table (LUT) stored in a ROM with an input address to retrieve a LUT output, wherein:

the input address corresponds to current status of one or more of the FIFOs; and

the LUT output identifies a next FIFO to select for service

and whether the FIFO has data available for service; storing and forwarding the LUT output based on a received latch enable (LE) control signal;

with a finite state machine (FSM) implemented using combinatorial feedback logic;

10

generating the LE control signal based on the forwarded LUT output; and

generating a read enable (RE) control signal that identifies which one or more of FIFOs are to be serviced, based on the forwarded LUT output.

9. The invention of claim 8, wherein the LE and RE control signals are generated using an FSM having an IDLE state and an EXTRACT state, wherein:

when the FSM is in the IDLE state and a currently selected FIFO has no data available for service, the LE control signal is set to forward the LUT output; and

when the FSM is in the EXTRACT state and service of the currently selected FIFO is completed, the LE control signal is set to forward the LUT output.

10. The invention of claim 9, wherein:

when the FSM is in the IDLE state and at least one FIFO has data available for service, the FSM transitions to the EXTRACT state; and

when the FSM is in the EXTRACT state and no FIFO has data available for service, the FSM transitions to the IDLE state.

11. The invention of claim 8, wherein the current status of the one or more FIFOs comprises an indication of whether each FIFO has data available for service and an indication of which FIFO is currently selected for service.

12. The invention of claim 11, wherein a FIFO has data available for service when the FIFO currently stores more than a specified threshold number of data packets.

13. The invention of claim 8, wherein the LE and RE control signals are generated based on service status information from the FIFOs.

14. The invention of claim 13, wherein the service status information comprises an indication of completion of service of the currently selected FIFO.

\* \* \* \* \*